

Table 4-11 Pin assignments for the PCI-bus communications slot connector

Odd-numbered pins	Function	Even-numbered pins	Function
1	/DCD	2	/DTR
3	/CTS	4	/RTS
5	RxD	6	TxD
7	IN_SENSE	8	SCC_ENAB
9	INT_MIC	10	MIC_SENSE
11	MIC_RET	12	EXT_AUD_L
13	Reserved	14	EXT_AUD_RET
15	GND	16	+12V
17	-5V	18	+12V
19	SYS_WAKEUP	20	Trickle +5
21	GND	22	GND
23	A1	24	A0
25	A3	26	A2
27	+3.3V	28	+3.3V
29	A5	30	A4
31	A7	32	A6
33	+5V	34	+5V
35	A8	36	C/BE(0)~
37	A10	38	A9
39	GND	40	GND
41	A12	42	A11
43	A14	44	A13
45	C/BE(1)~	46	A15
47	GND	48	Gnd
49	SERR~	50	PAR
51	PERR~	52	SBO~
53	LOCK~	54	SDONE
55	+3.3V	56	+3.3V
57	DEVSEL~	58	STOP~
59	IRDY~	60	TRDY~
61	+5V	62	+5V
63	C/BE(2)~	64	FRAME~
65	A17	66	A16
67	GND	68	GND
69	A19	70	A18
71	A21	72	A20
73	A23	74	A22
75	GND	76	GND
77	C/BE(3)~	78	IDSEL
79	A25	80	A24
81	A27	82	A26
83	+3.3V	84	+3.3V
85	A29	86	A28
87	A31	88	A30
89	+5V	90	+5V
91	REQ~	92	GNT~
93	+5V	94	+5V
95	INT~	96	Reserved
97	Reserved	98	RST~
99	GND	100	Reserved'
101	CLK	102	Reserved
103	GND	104	Reserved
105	Reserved	106	Reserved
107	Reserved	108	Reserved
109	CommGnd	110	RefGnd
111	AudToSlot	112	AudFromSlot