

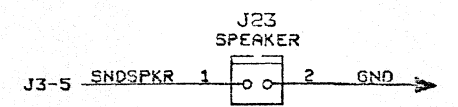
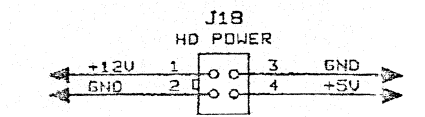
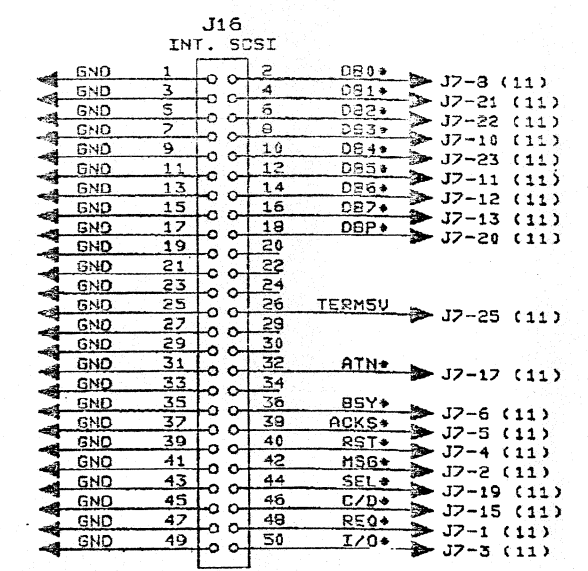
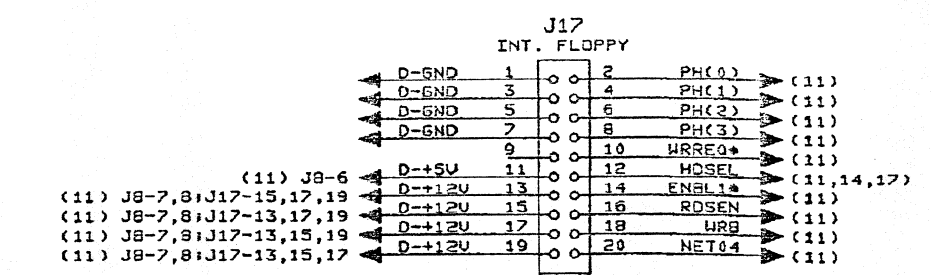
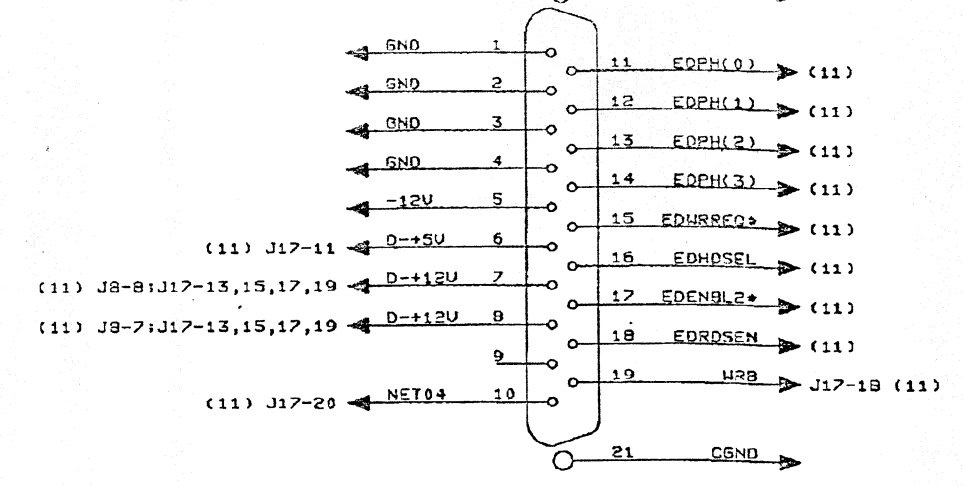
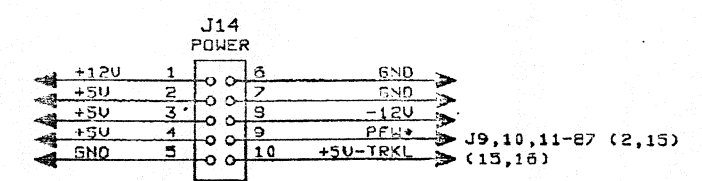
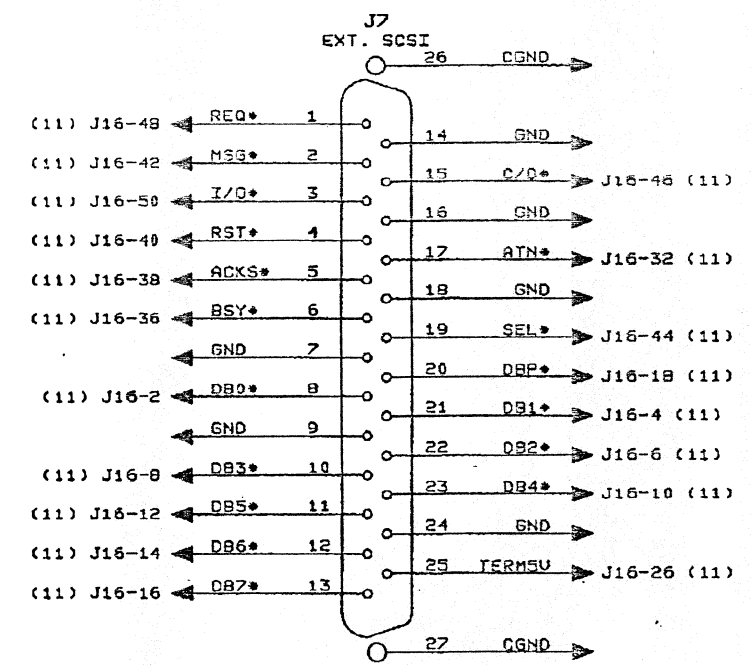
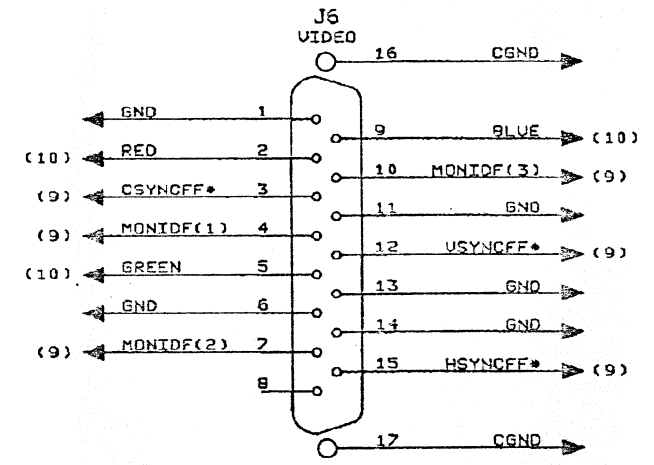
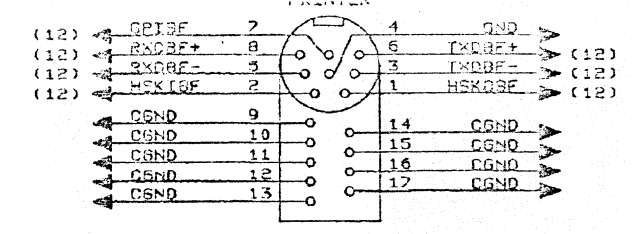
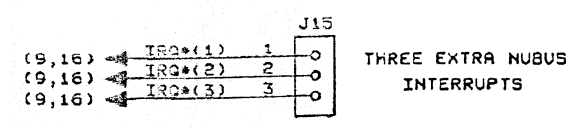
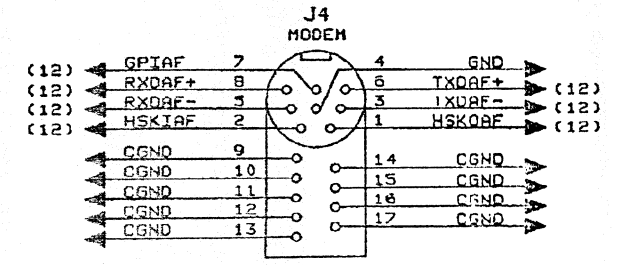
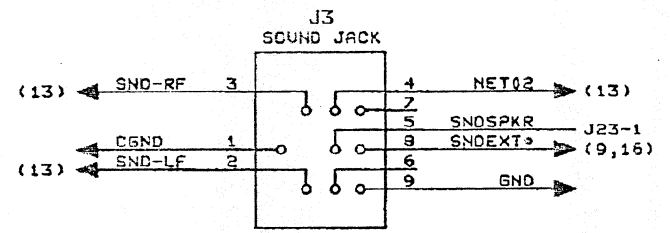
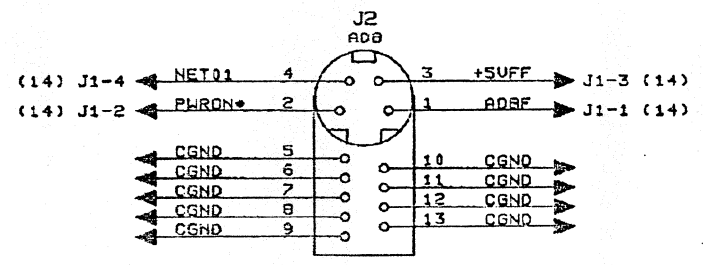
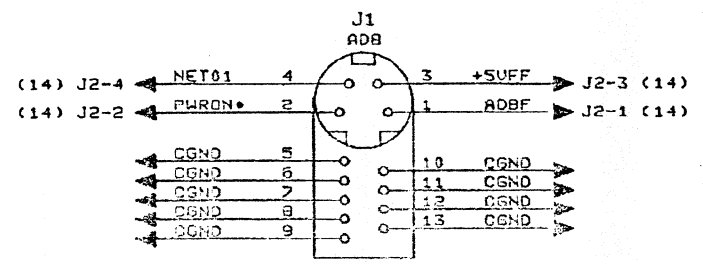


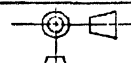
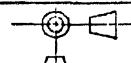
IIci

C V286

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTAL VALUES ARE IN HERTZ.
4. () DENOTE SHEET TO SHEET REFERENCES.
5. ON J9, J10, & J11, -5.2V PINS ARE SHORTED TOGETHER, BUT -5.2V IS NOT SUPPLIED TO THEM.
6.  INDICATES SIGNALS WHICH GO TO A CONNECTOR.
7.  INDICATES SIGNALS WHICH GO ONLY TO A TEST POINT.



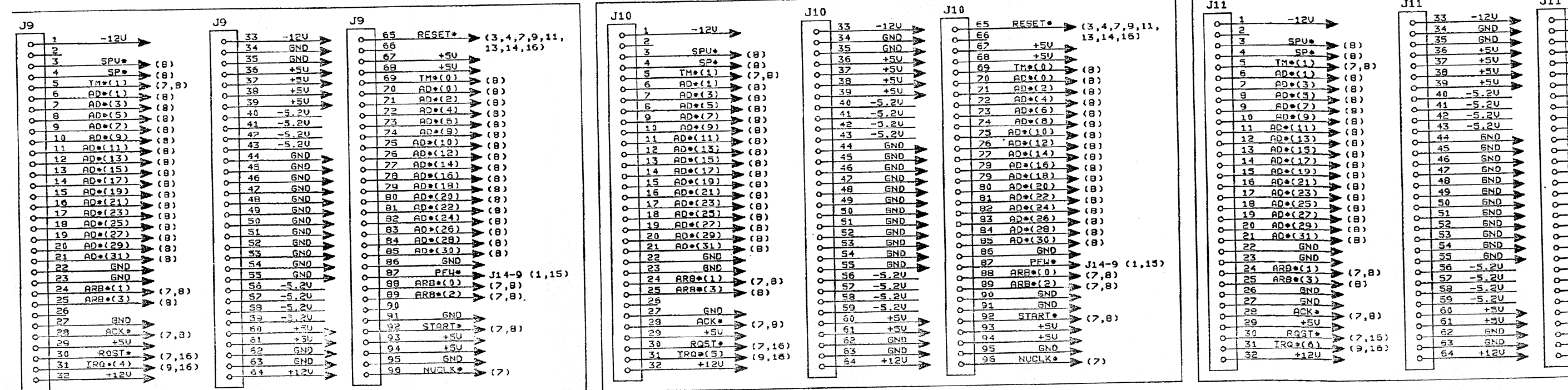
REFEREN
LAST I
BT1, C158,
DS1, F3, J
Q4, R112,
S3, IP2/B,

DIMENSIONS ARE IN MILLIMETERS		TOLERANCES	
XXX =		XXX =	
XXX =		XXX =	
XXX =		XXX =	
ANGLES =		ANGLES =	
DO NOT SCALE DRAWINGS		DO NOT SCALE DRAWINGS	
			
<p>DRAWN BY: <i>RANTON</i></p> <p>CHECKED BY: <i>WPA</i></p> <p>DATE: <i>10/1/86</i></p> <p>REVISION: <i>1</i></p> <p>APPROVED BY: <i>Cooper</i></p>		<p>MATERIAL</p>	

SLOT1=C

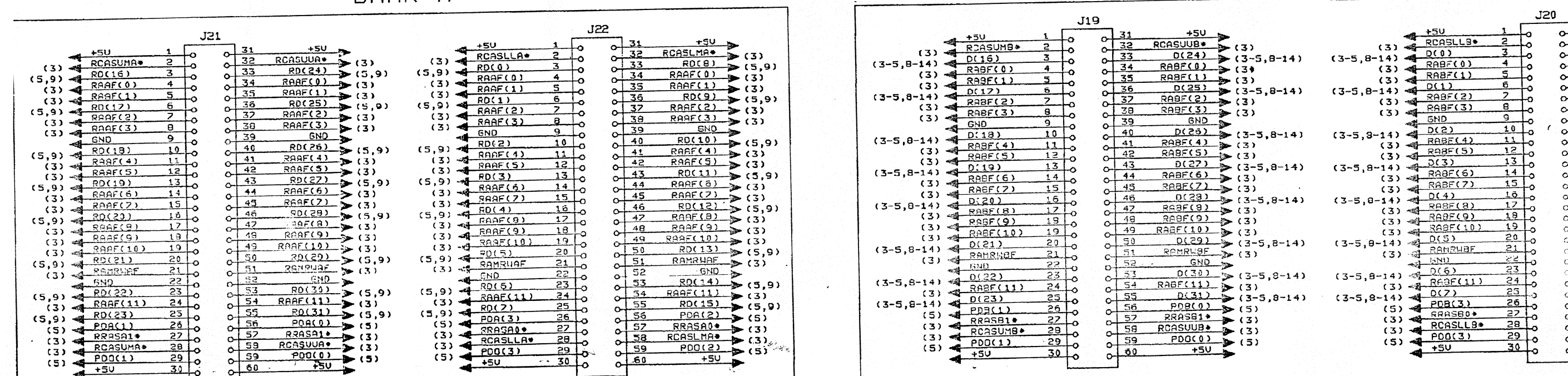
SLOT2=D

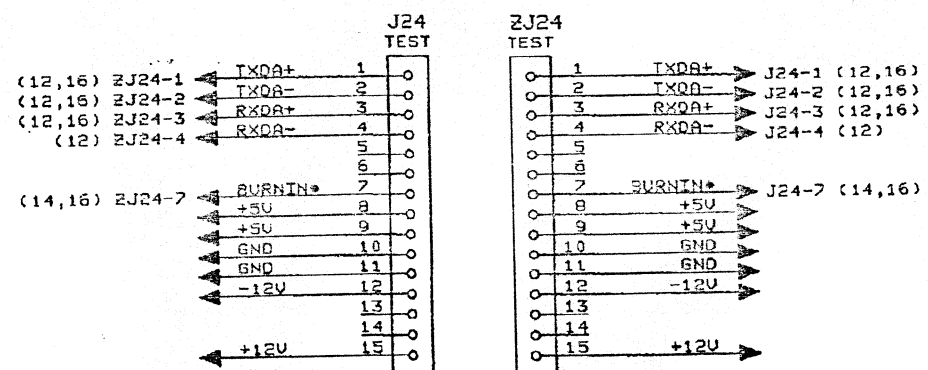
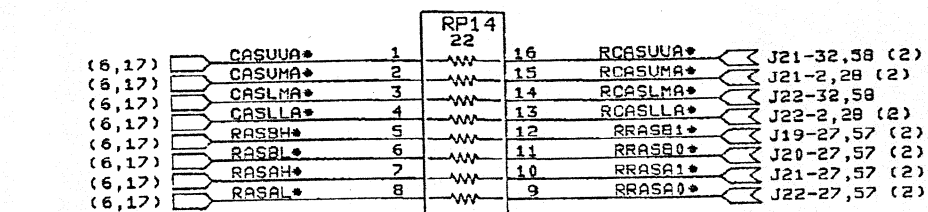
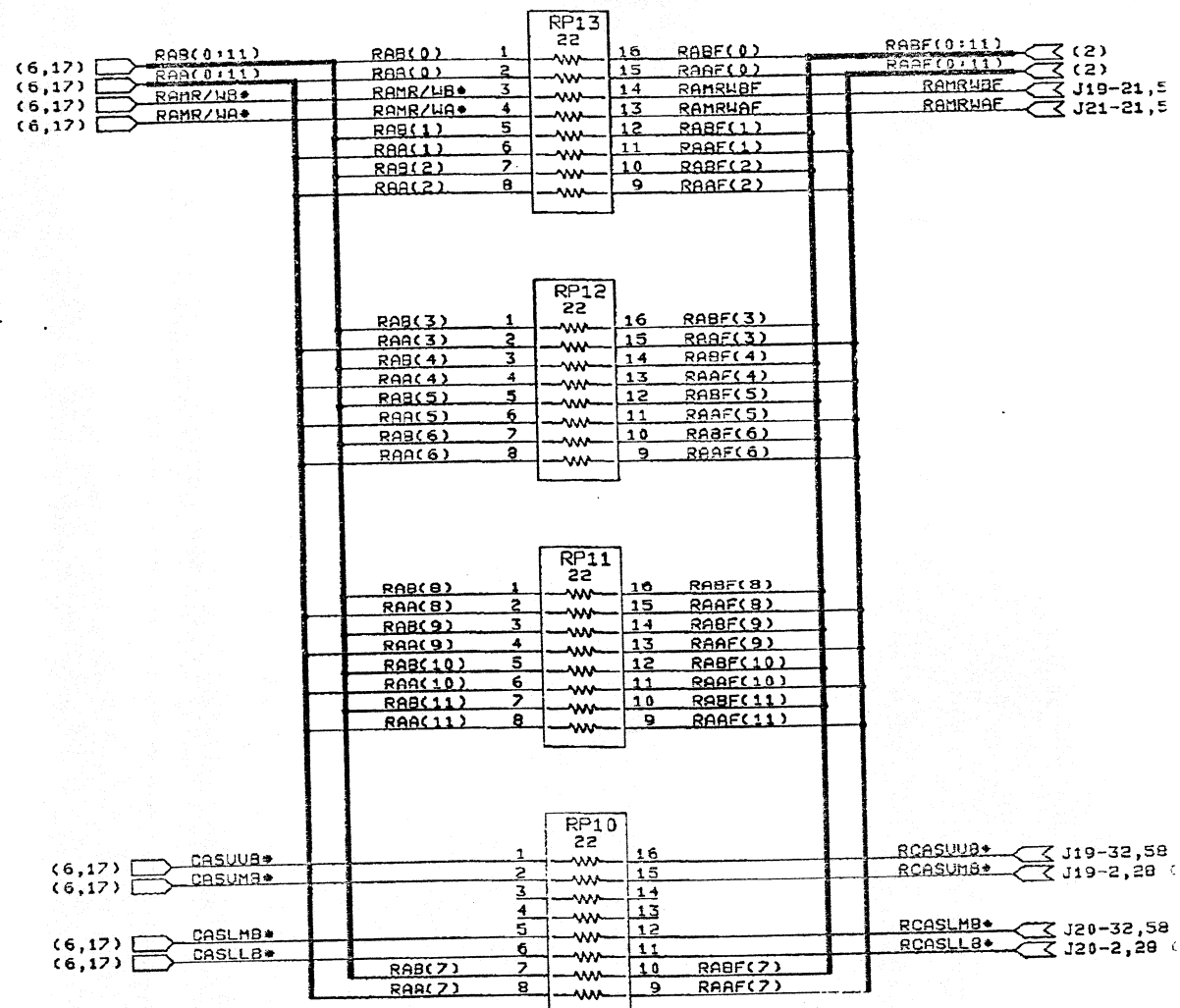
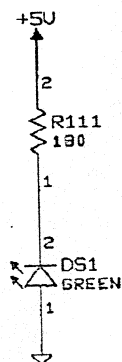
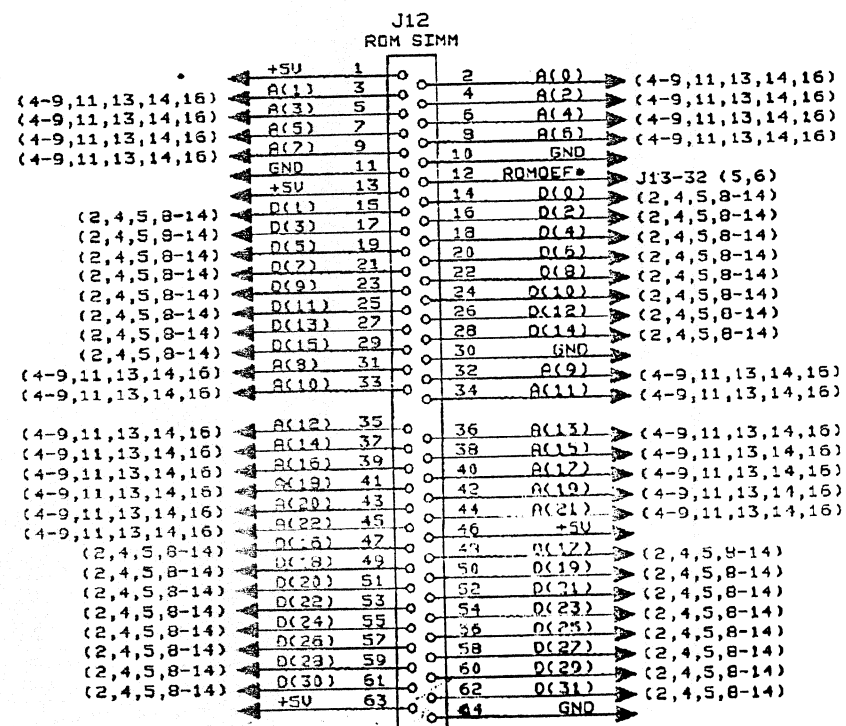
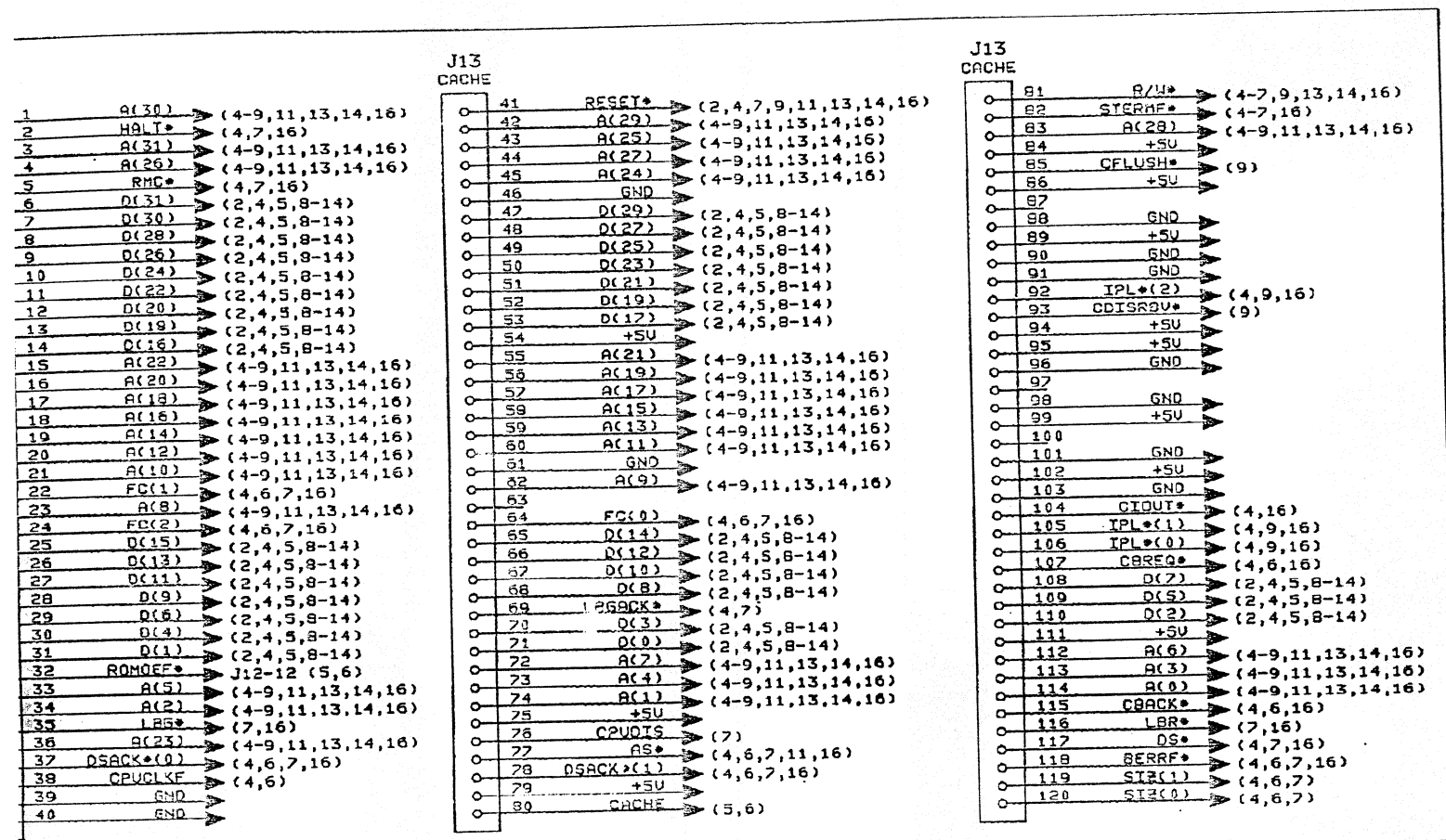
SLOT3=E



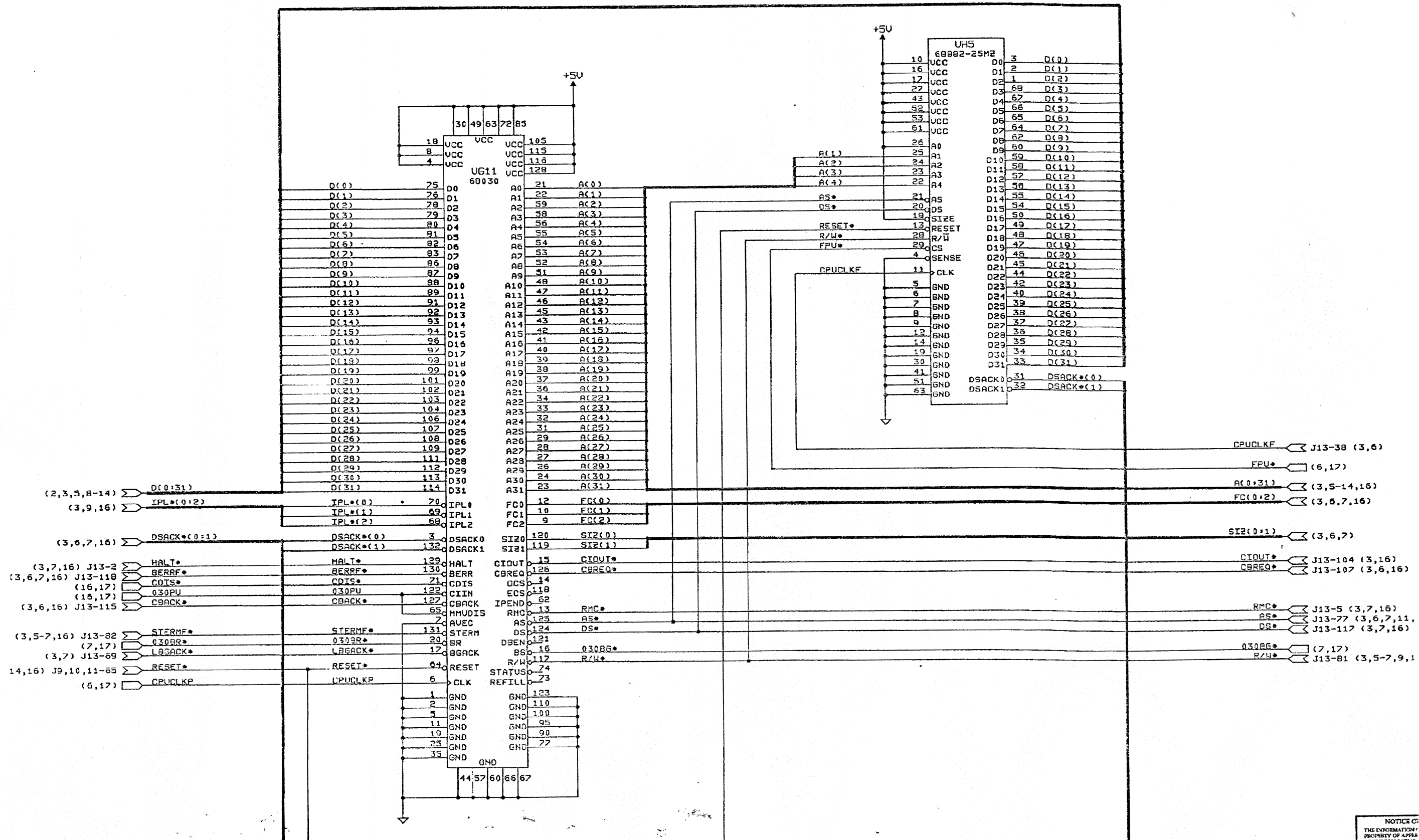
BANK A

BANK B

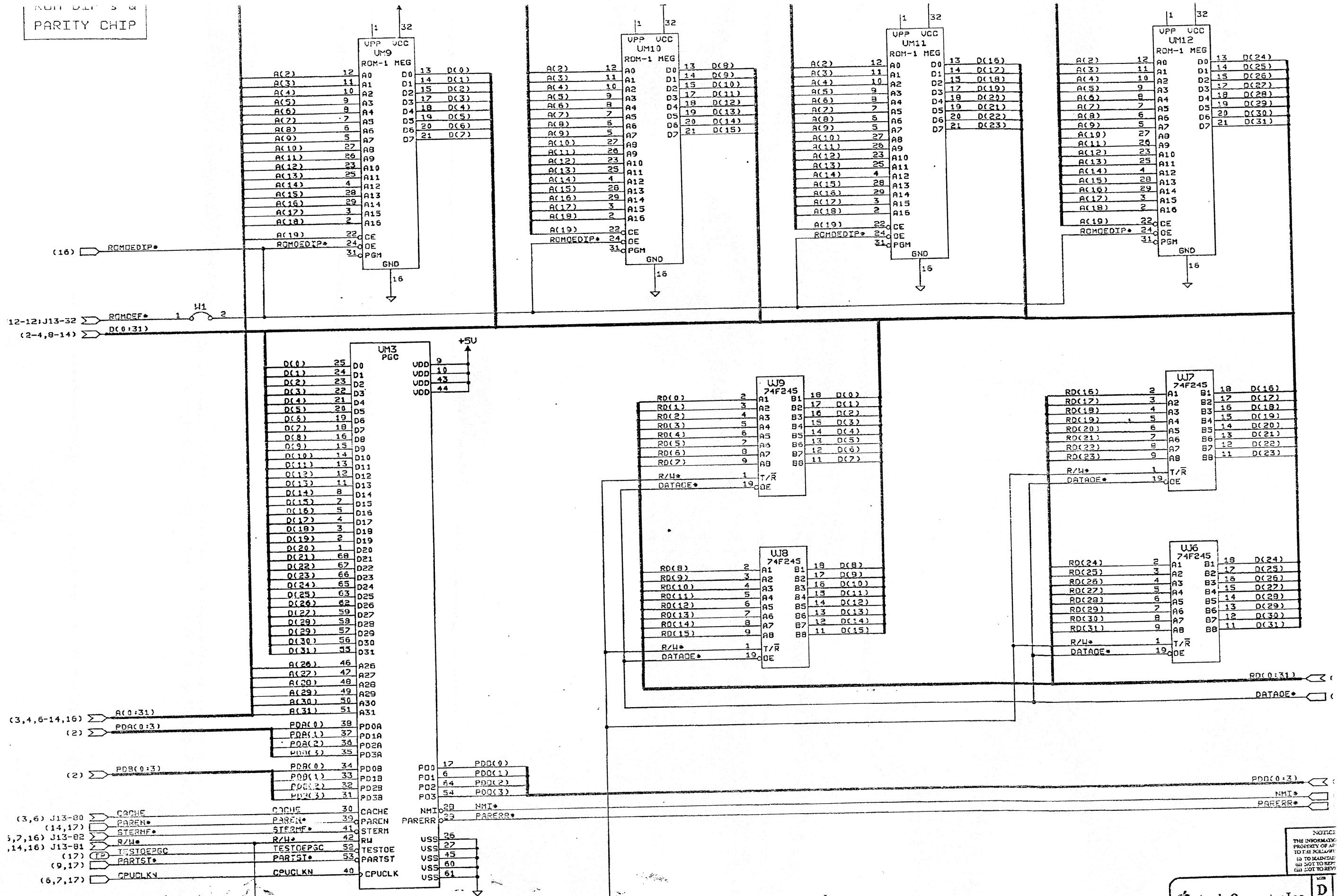


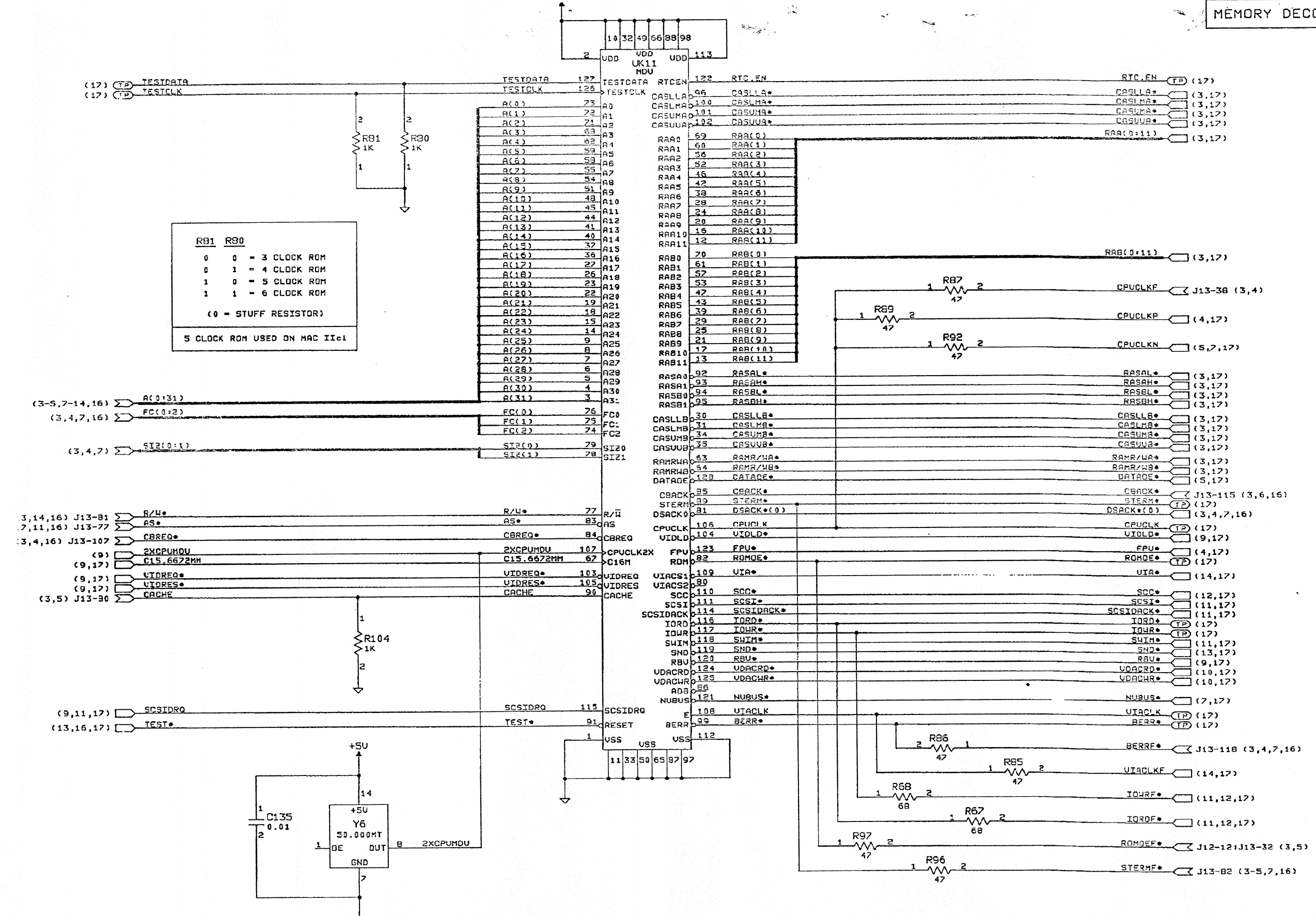


CPU (68030) & FPU (68882)



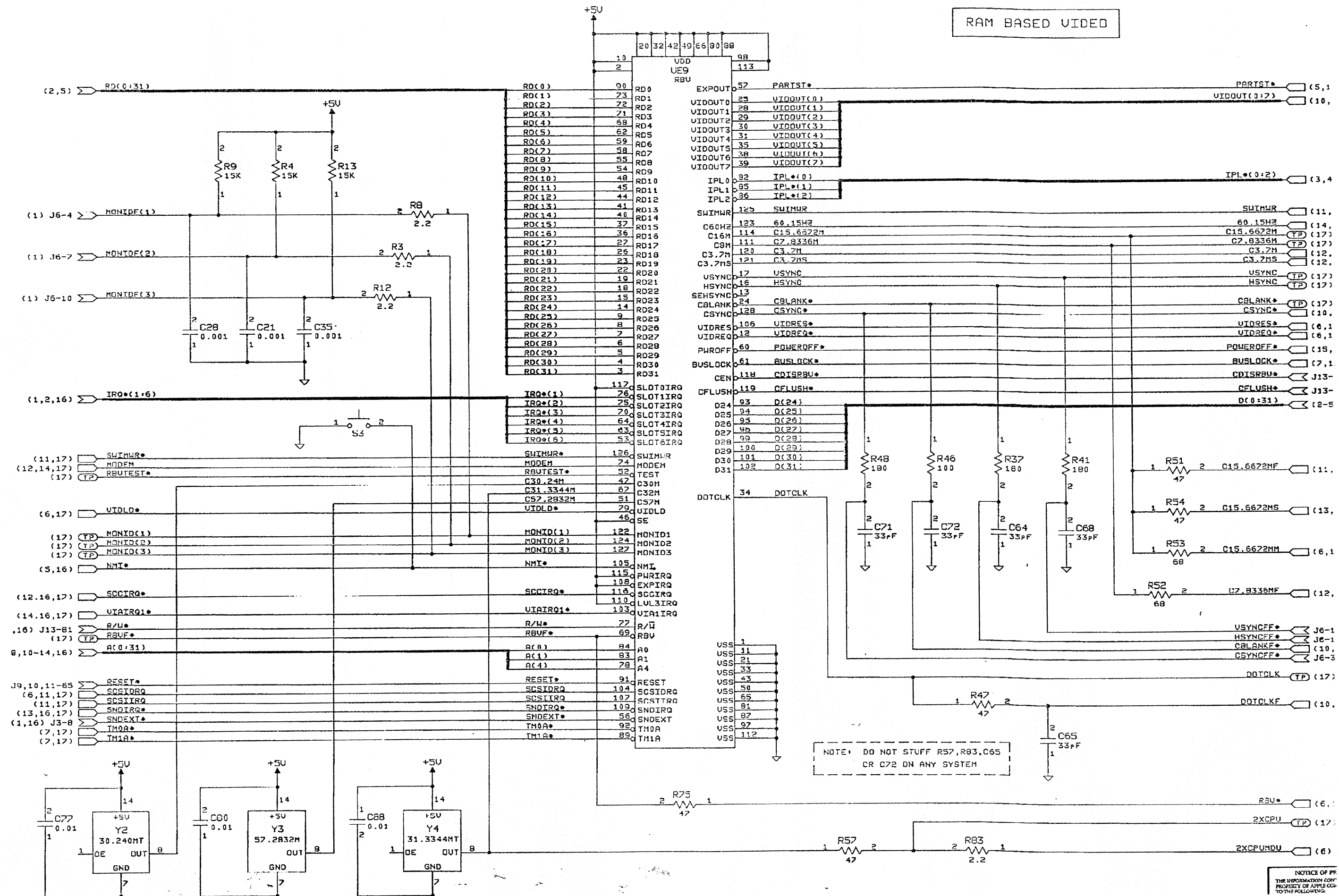
ROM DIP 3 & PARITY CHIP



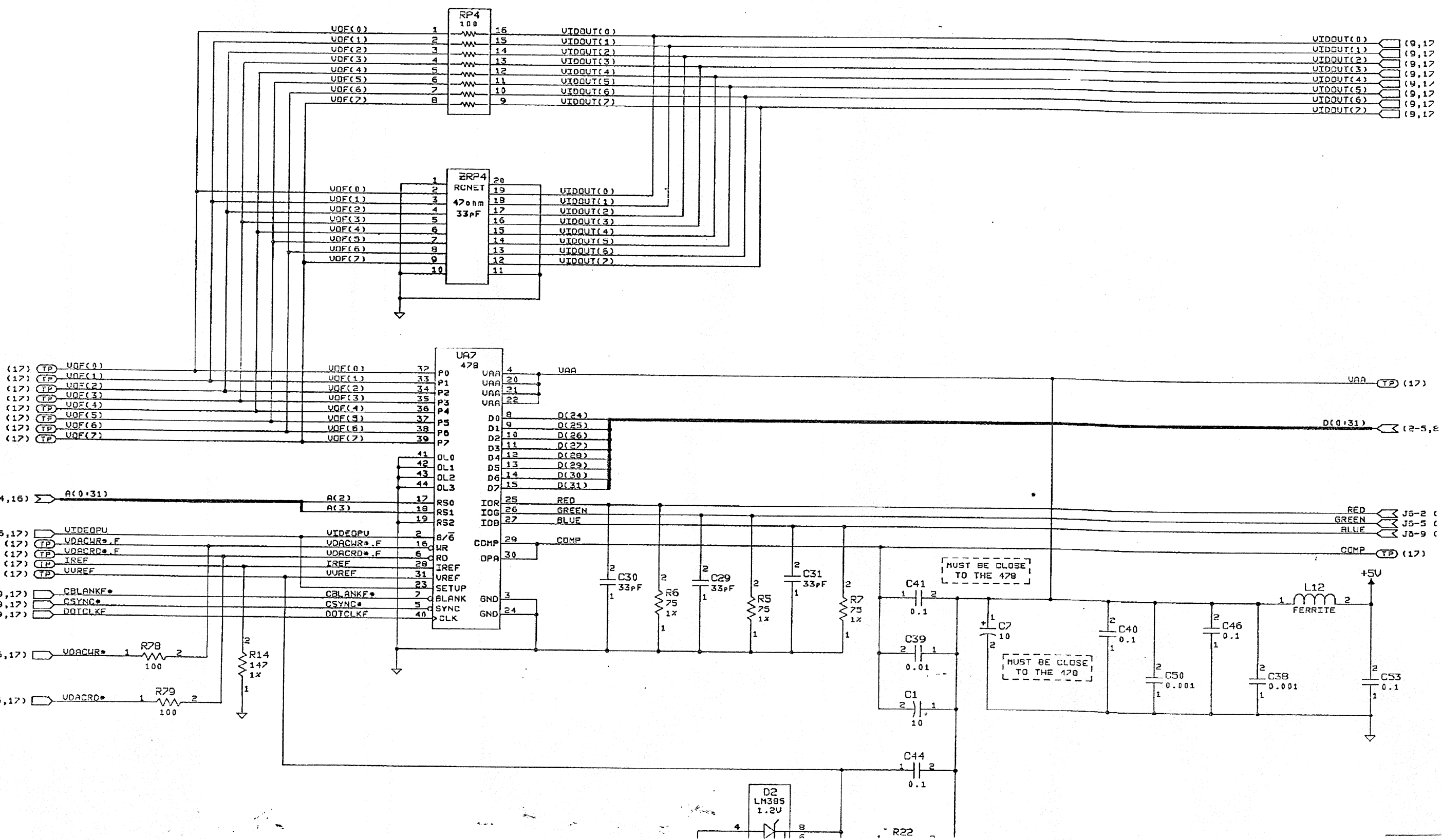


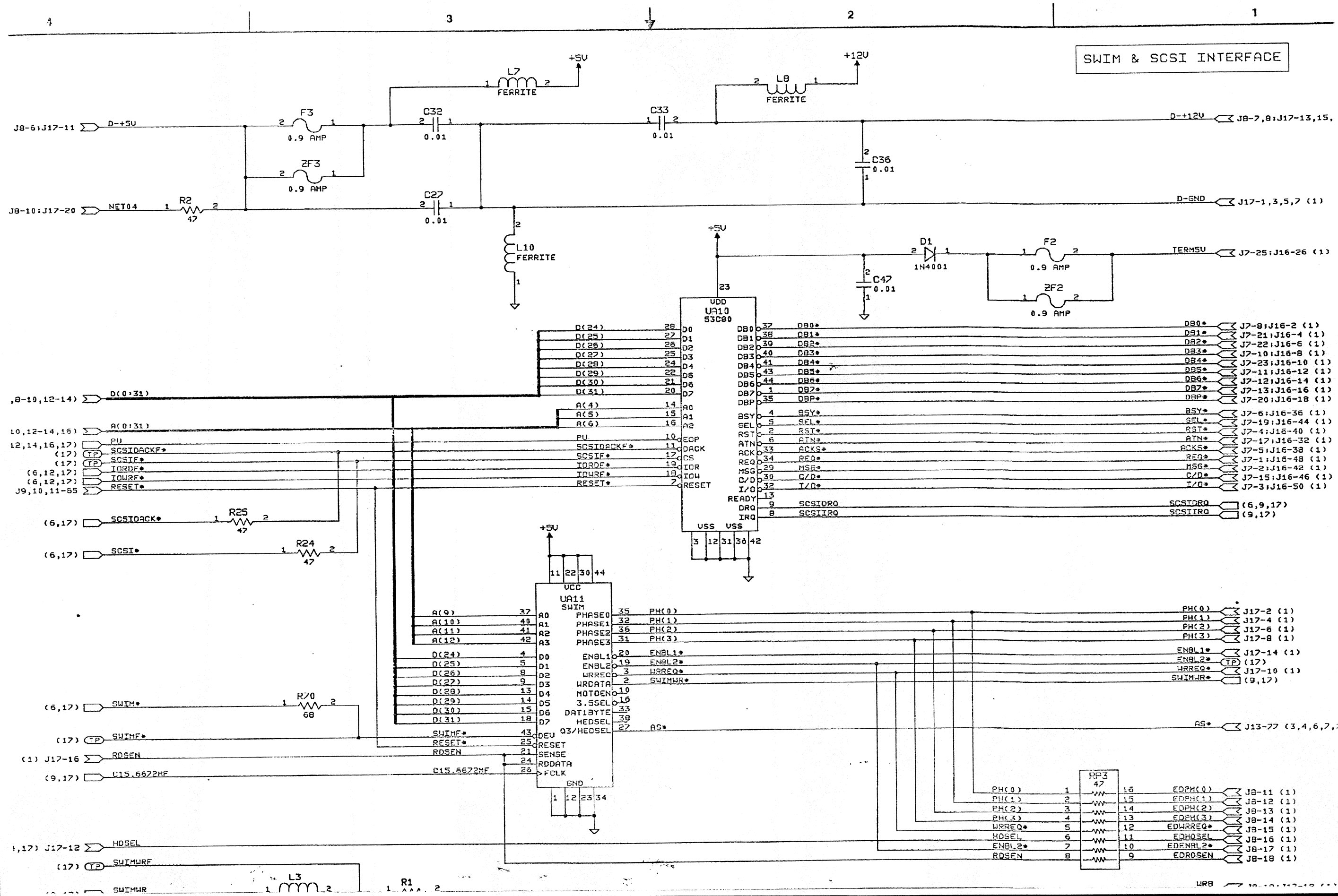
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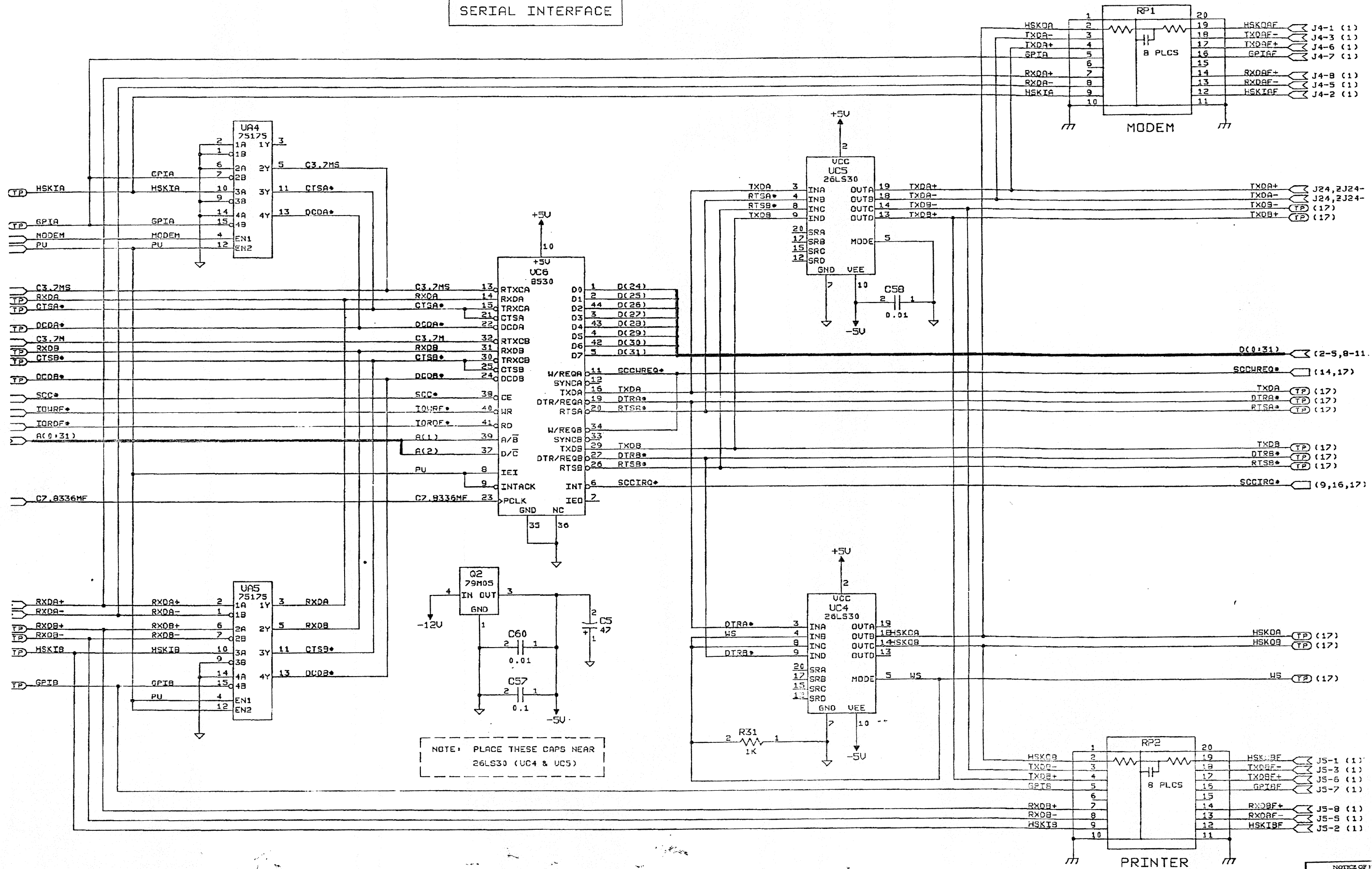


VIDEO DAC

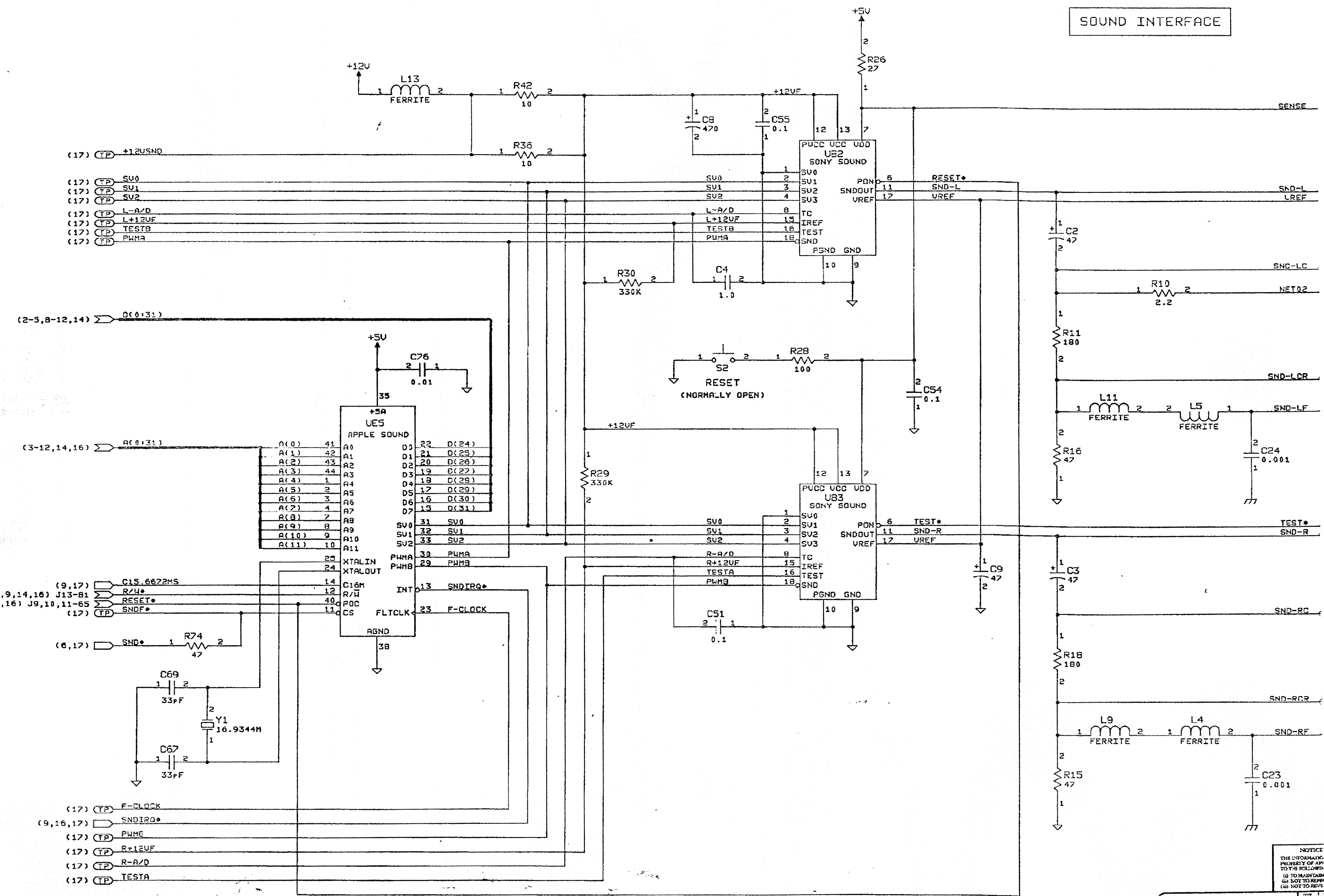




SERIAL INTERFACE

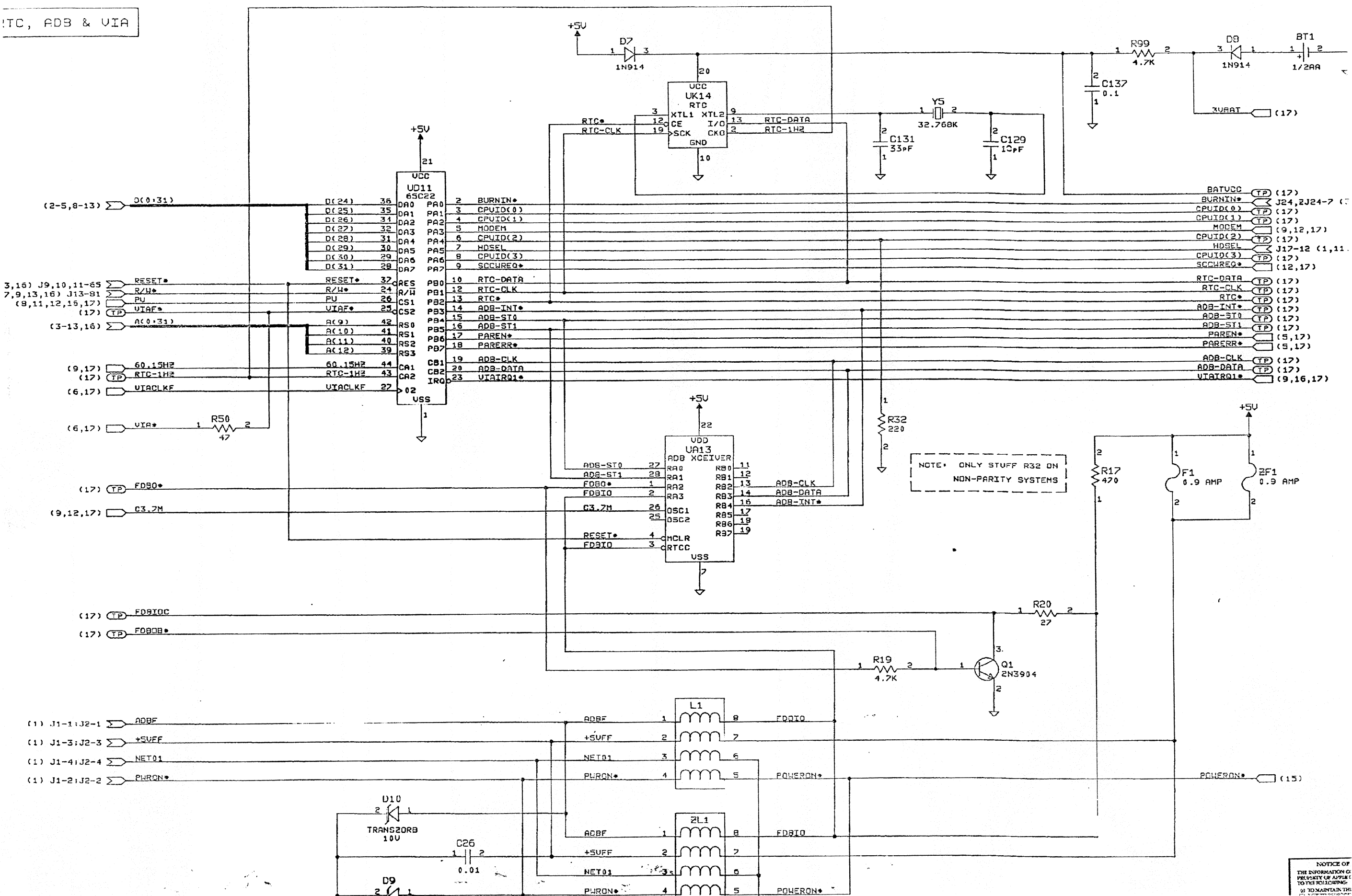


SOUND INTERFACE



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(c) NOT TO REVEAL

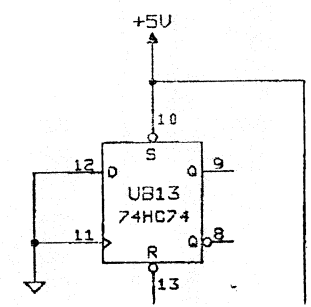
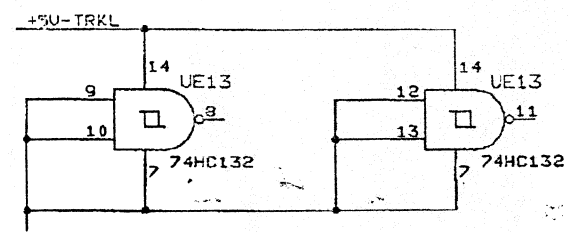
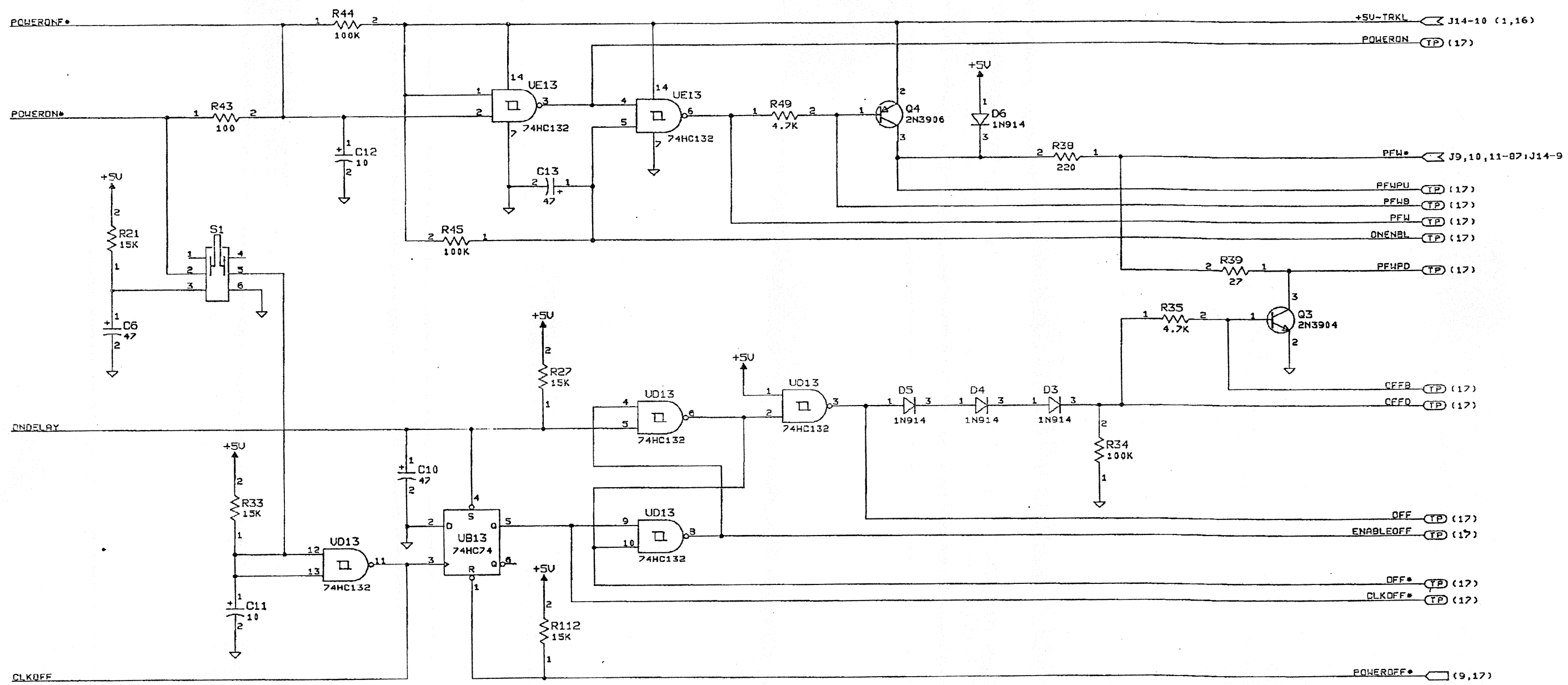
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