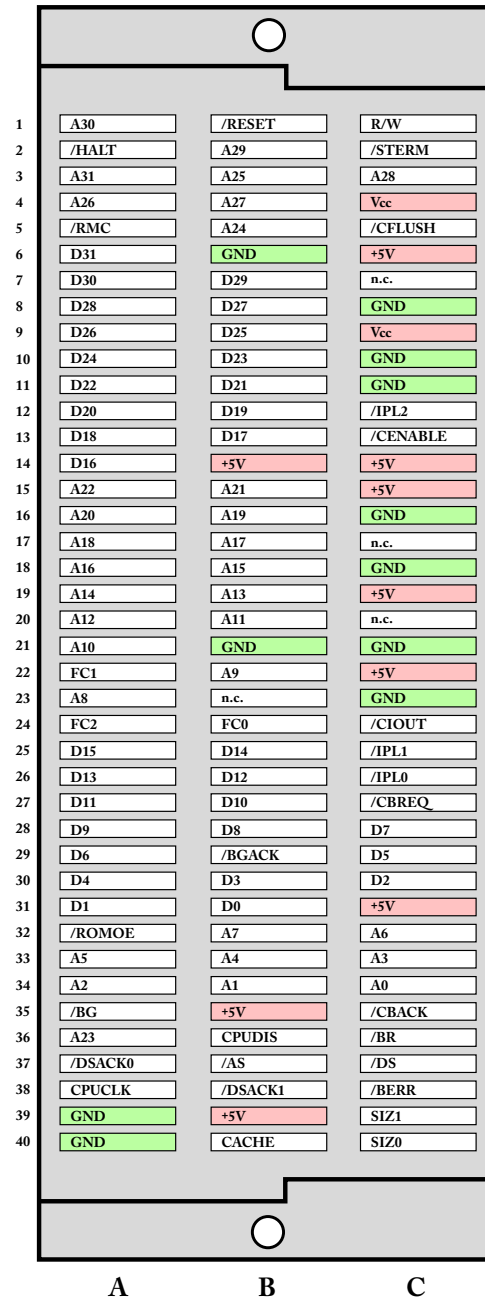


## Cache Connector

### Macintosh IIci Cache Connector Signal Descriptions

Signal name	Signal description
AO-A31	Address bus, bits 0 through 31
DO-D31	Data bus, bits 0 through 31
/RESET	System reset
/BERR	Bus error
/HALT	Halt
FC0-FC2	Function codes, bits 0 through 2
/BR	Bus request
/BG	Bus grant
/BGACK	Bus grant acknowledge
SIZ0-SIZ1	Transfer size, bits 0 and 1
/AS	Address strobe
/DSACK0 -/DSACK1	Data transfer & size acknowledge, bits 0 & 1
R/W	Read/write
/STERM	Synchronous termination
/CBACK	Cache burst acknowledge
/CBREQ	Cache burst request
/CIOUT	Cache inhibit out
/DS	Data strobe
/RMC	Read-modify-write cycle
/IPL0 -/IPL2	Interrupt priority lines, bits 0 through 2
CPUCLK	25 MHz CPU clock
/ROMOE	ROM output enable
CPUDIS	CPU disable
CACHE	Memory controller for cache access
CFLUSH	Cache flush
CENABLE	Cache enable
n.c.	No connection
+5V	+5 volts
GND	Ground



## SE/30 PDS Connector

### Macintosh SE/30 PDS Connector Descriptions

Signal name	Signal description
AO-A31	Address bus, bits 0 through 31
FC0-FC2	Function Code
DO-D31	Data bus, bits 0 through 31
/RESET	System reset
/BERR	Bus error
/HALT	Halt
FC0-FC2	Function codes, bits 0 through 2
/BR	Bus request
/BG	Bus grant
/BGACK	Bus grant acknowledge
SIZ0-SIZ1	Transfer size, bits 0 and 1
/AS	Address strobe
/DSACK0 -/DSACK1	Data transfer & size acknowledge, bits 0 & 1
R/W	Read/write
/STERM	Synchronous termination
/CBACK	Cache burst acknowledge
/CBREQ	Cache burst request
/CIOUT	Cache inhibit out
/DS	Data strobe
/RMC	Read-modify-write cycle
/IPL0 -/IPL2	Interrupt priority lines, bits 0 through 2
CPUCLOCK	16.67 MHz CPU clock
+5V	+5 volts
GND	Ground
PWROFF	Power off
NUBUS	NuBus space access
BUSLOCK	NuBus buslock
/TM0A -/TM1A	NuBus transfer mode, bits 0 & 1
IRQ1-IRQ3	Interrupt inputs
ECLK	E clock
C16M	15.6672MHz gen clock

